



Appl. No. 09/829,793
Amendment and/or Response
Reply to Office Action of May 8, 2003

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2186
8/A
C. Bane
10/31/03

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

Appl. No.: 09/829,793
Applicant(s): M. Emons et al.
Filed: April 10, 2001
TC/A.U.: 2100/2186
Examiner: H. Kim
Atty. Docket: NL 000215
Title: Data Processing Circuit with
a Cache Memory and Apparatus Containing
Such a Circuit

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On: August 8, 2003

By: Michelle Welgoss
Michelle Welgoss

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Technology Center 2100

AMENDMENT and/or RESPONSE under 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of May 8, 2003, please
amend the above referenced application as follows and
reconsider the application in light of the following remarks.

This paper includes (each beginning on a separate sheet):

- 1. Amendments to the claims reflected in the complete listing of the claims**, which begins on page 3 of this paper;
- 2. Amendments to the Specification**, which begin on page 6 of this paper;
- 3. Amendments to the Drawings**, which begin on page 7 of this paper; and
- 4. Remarks/Discussion of issues**, which begin on page 8 of this paper;